



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT(S):

Chan et al.

SERIAL NO.:

10/645,209

GROUP NO.:

Not yet assigned

FILING DATE:

August 21, 2003

EXAMINER:

Not yet assigned

TITLE:

Advance Failure Prediction

CERTIFICATE OF FIRST CLASS MAILING UNDER 37 C.F.R. 1.8

I hereby certify that this correspondence, and any document(s) referred to as enclosed herein, is/are being deposited with the United States Postal Service as first class mail, postage prepaid, in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on this 30 day of September, 2003.

Susan Boucher

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Submitted herewith is/are:

Transmittal Form (1 page); Information Disclosure Statement (3 pages); PTO Form 1449 (2 pages); copies of cited references (A-A5, B1-B2, C1-C11); and a return-receipt postcard.

01	PE								
<u> </u>	6		Application Se	erial Number	10/645	5,209			
OCT 0 3 2003 (2)			Filing Date		August 31, 2003				
			First Named Inventor		Chan				
			Group Art Unit		Not yet assigned				
		سلا.	Examiner Name		Not yet assigned				
	FORM		Attorney Dock	orney Docket No.		IBX-004			
			Patent No.		Not applicable				
			Issue Date		Not ap	pplicable			
ENCLOSURES (check all that apply)									
☐ Fe	e Transmittal Form		Copy of Notice t	o File Missing		Notice of Appeal to Board			
L 10	ļ	_	Parts of Applicat	tion (PTO-1553)		of Patent Appeals and Interferences			
	☐ Check Attached☐ Copy of Fee Transmittal Form		Formal Drawing	(s)		Appeal Brief (in triplicate)			
	Amendment/Response		Request For Cor Examination (Re	ntinued CE)		Status Inquiry			
	☐ Preliminary ☐ After Final	Transmittal			\boxtimes	Return Receipt Postcard			
	Affidavits/declaration(s) Letter to Official Draftsperson		Power of Attorn (Revocation of I	ey Prior Powers)		Certificate of First Class Mailing under 37 C.F.R. 1.8			
	including Drawings [Total Sheets]		Terminal Discla	imer		Certificate of Facsimile Transmission under 37 C.F.R. 1.8			
	Petition for Extension of Time			ration and Power Utility or Design ion		Additional Enclosure(s) (please identify below)			
⊠	Information Disclosure Statement		Small Entity Sta	atement	:				
	Form PTO-1449 Copies of IDS Citations		CD(s) for large program	table or computer					
	Certified Copy of Priority		Amendment Af	ter Allowance					
	Document(s)		Request for Certificate of Correction Certificate of Correction (in duplicate)						
CORI	RESPONDENCE ADDRESS			SIGNATURE BI	OCK_	D			
Direct all correspondence to: Patent Administrate Testa, Hurwitz & T High Street Tower 125 High Street Boston, MA 0211 Tel. No.: (617) 248 Fax No.: (617) 248			hibeault, LLP) -7000	Date: September ² / ₂ Reg. No. 33,497 Tel. No.: (617) 310 Fax No.: (617) 790	0-8108	Respectfully submitted, Steven J. Frank Attorney for Applicant(s) Testa, Hurwitz & Thibeault, LLP High Street Tower 125 High Street Boston, MA 02110			



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Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

In accordance with the provisions of 37 C.F.R. 1.97 and 1.98, Applicants hereby make of record the patents and publications listed on the accompanying Form PTO-1449, and other information contained herein, for consideration by the Examiner in connection with the examination of the above-identified patent application. Copies of the patents and publications are enclosed.

REMARKS

In accordance with the provisions of 37 C.F.R. 1.97, this statement is being filed (CHECK ONE):

(1)	within three (3) months of the filing date of a national application other than a continued prosecution application under 37 C.F.R. 1.53(d), or within three (3) months of the date of entry of the national stage as set forth in 37 C.F.R. 1.491 in an international application, or before the mailing of the first Office action on the merits, or before the mailing of a first Office action after the filing of a request for continued examination under 37 C.F.R. 1.114; or
(2)	after the period defined in (1) but before the mailing date of a final action or a notice of allowance under 37 C.F.R. 1.311, and
	the requisite Statement is below, OR
	the requisite fee under 37 C.F.R. 1.17(p), namely \$180.00, is included herein, or

Information Serial No. Page 2 of	10/645,2	ure Statement 09
	(3)	after the mailing date of a final action or notice of allowance but before the payment of the issue fee, AND
		the requisite Statement is below, AND
		the requisite petition fee under 37 C.F.R. 1.17(p), namely \$180.00 is included herein.
		fully requested that each of the patents and publications listed on the attached Form
PTO-14	49, and	other information contained herein, be made of record in this application.
		STATEMENT
		d under 37 C.F.R. 1.97(e), Applicant(s), through the undersigned, hereby state either that
[check	the appi	opriate space only if either (2) or (3) is checked on the previous page <u>and</u> the
Statem	ent is re	quired]:
		1. Each item of information contained in the Information Disclosure Statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the Information Disclosure Statement; or
		2. No item of information contained in the Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the person signing this Statement after making reasonable inquiry, no item of information contained in the Information Disclosure Statement was known to any individual designated in 37 C.F.R. 1.56(c) more than three months prior to the filing of the Information Disclosure Statement.
		n, Applicant wishes to inform the Examiner about the following co-pending patent blications, issued patents, and Office Actions issued therein:
τ	J.S. Seri	al No. 10/243,963, filed on September 13, 2002, by Card et al. (IBX-001);
τ	J.S. Seri	al No. 10/244,154, filed on September 13, 2002, by Reitman et al. (IBX-002);
Ţ	IS Seri	al No. 10/621.532, filed on July 17, 2003, by Card et al. (IBX-005); and

U.S. Serial No. 10/656,607, filed on September 5, 2003, by Card et al. (IBX-008).

Information Disclosure Statement Serial No. 10/645,209 Page 3 of 3

Date: September <u>40</u>, 2003 Reg. No. 33,497

Tel. No.: (617) 310-8108

Fax No.: (617) 248-7100

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Respectfully submitted,

Attorney for the Applicants Testa, Hurwitz, & Thibeault, LLP

High Street Tower 125 High Street

Boston, Massachusetts 02110



FORM PTO - 1449

INFORMATION DISCLOSURE STATEMENT

ATTORNEY DOCKET NO.: IBX-004

APPLICANT(S): Chan et al.

SERIAL NO.: 10/645,209

					FILING DA	ATE: 8/2	1/03 GR	ROUP: N	ot yet	assigned
			U.S.	PATENT	DOCUME	ENTS				
EXAM. INIT.		DOCUMENT NUMBER	DATE	NAME			CLASS	SUB CLASS		NG DATE IF ROPRIATE
	A1	5,467,883	11/21/95	Frye et al.			216	60	11/2	7/93
-	A2	5,559,690	9/24/96	Keeler et a	al.		364	164	9/16	/94
	A3	5,654,903	8/5/97	Reitman e	t al.		364	551.01	11/7	/95
	A4	5,740,033	4/14/98	Wassick e	Wassick et al.			149	10/13/92	
	A5	6,268,226	7/31/01	Angell et al.			438	16	6/30/99	
			FOREI	GN PATE	NT DOCU	MENTS	<u> </u>		<u> </u>	
EXAM. INIT.		DOCUMENT NUMBER	DATE	COUNTRY		SUB CLASS	FILING DATE	ONLY		ENGLISH LANG (Y/N)
	B1	WO 01/57605	8/9/01	wo	G05B	13/04	1/11/01	N		Y
	B2	DE196 37 917 A1	3/19/98	DE	G05B	13/04	9/17/96	Y	···	
					NAL ART					
EXAM. INIT.	OTH	IER DOCUMENTS	S: (Includin	g Author, T	itle, Date, Re	elevant Pa	ges, Place	of Publica	tion)	
C1 Card et al., "Dynamic Neural Control for Plasma Etch Process," IEEE Transactions on Neural Networks (1997). C2 Dillon et al., "Guest Editorial Everyday Applications of Neural Networks," IEEE Transactions on Neural Networks, 8:4 (1997). C3 Hatzipantelis et al., "Comparing Hidden Markov Models with Artificial Neural Network Architectures of Condition Monitoring Applications," Artificial Neural Networks, 26-28, Conference Publication No. 4 (June 1995).										
						on Neural				
						ion No. 409				
	C4	Kim et al., "Intel Transactions on	ligent Contro Semiconduct	ol of Via Fori or Manufact	nation by Pharing, 12:503	otosensitiv (1999).	e BCB for	MCM-L/I) Appli	cations," <u>IEEE</u>
	C5 Konstantopoulos et al., "Controllers with Diagnostic Capabilities. A Neural Network Implementation. Journal of Intelligent and Robotic Systems," Department of Electrical Engineering, University of Notre Dame, IN 12: 197-228 (1995).					entation. of Notre				

DEMERS OF THE PARTY OF THE PART	C6	Moyne, A Research and Suppliers' Point of View," 3 rd Annual European AEC/APC Conference Proceedings (2002).				
	C7	Rietman et al., "A Study on $\Re^m \to \Re^1$ Maps: Application to a 0.16- μ m Via Etch Process Endpoint," IEEE (2000).				
	C8	Rietman et al., "A System Model for Feedback Control and Analysis of Yield: A Multistep Process Model of Effective Gate Length, Poly Line Width, and IV Parameters", <u>IEEE</u> (2001).				
	Rietman, "Neural Networks in Plama Processing," <u>Journal of Vacuum Science and Technology: Part B, IEEE Transactions on Semiconductor Manufacturing</u> , 14:1 (2001).					
	C10	Smyth et. al., "Hidden Markov Models an Neural Networks for Fault detection in Dynamic Systems," California Institute of Technology (1993).				
	C11	Zhang et al, "Control of Spatial Uniformity in Microelectronics Manufacturing: An Integrated Approach," Proceedings of AEC/APC (2000).				
EXAMI		DATE CONSIDERED				